

Dr. M. A. de Rooij



The eGaN<sup>®</sup> FET  
Journey Continues

Introducing eGaN<sup>®</sup> IC targeting Highly  
Resonant Wireless Power

*Efficient Power Conversion Corporation*

- Introducing the Synchronous Bootstrap FET
- eGaN ICs targeting Wireless Power
- Experimental results in a ZVS class D amplifier
- Summary

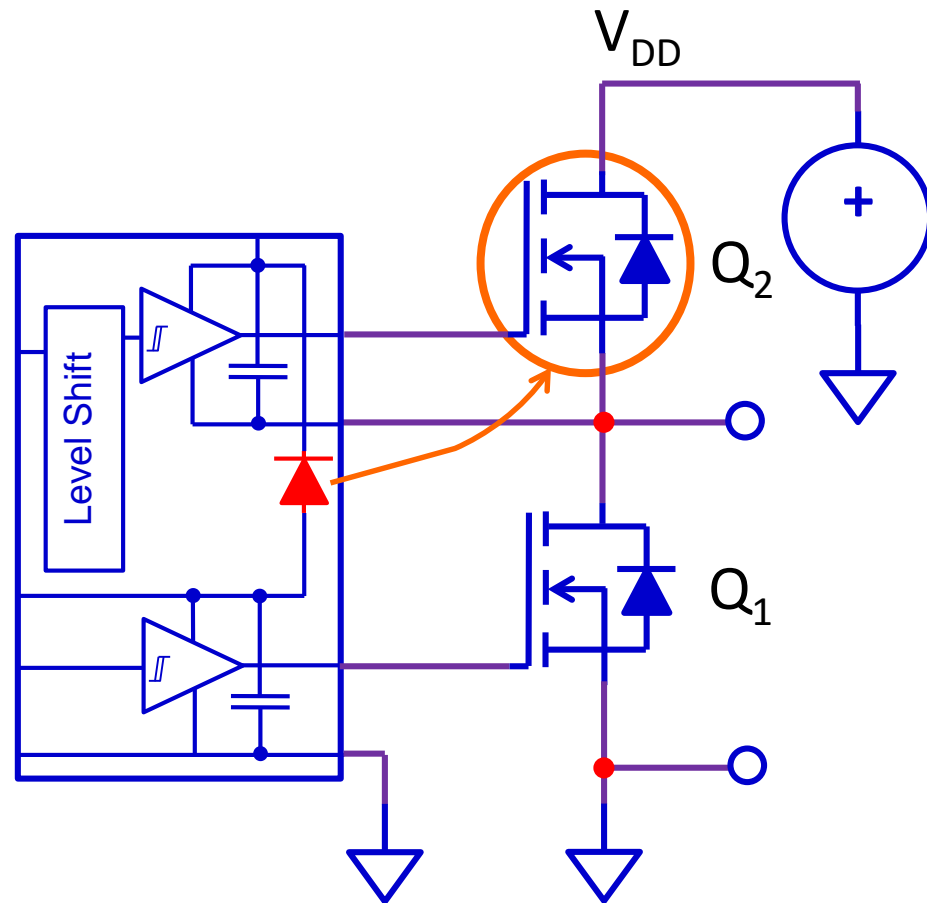
eGaN® is a registered trademark of Efficient Power Conversion Corporation

Gate drivers with internal bootstrap diodes have  $Q_{RR}$

- Schottky diode cannot be integrated

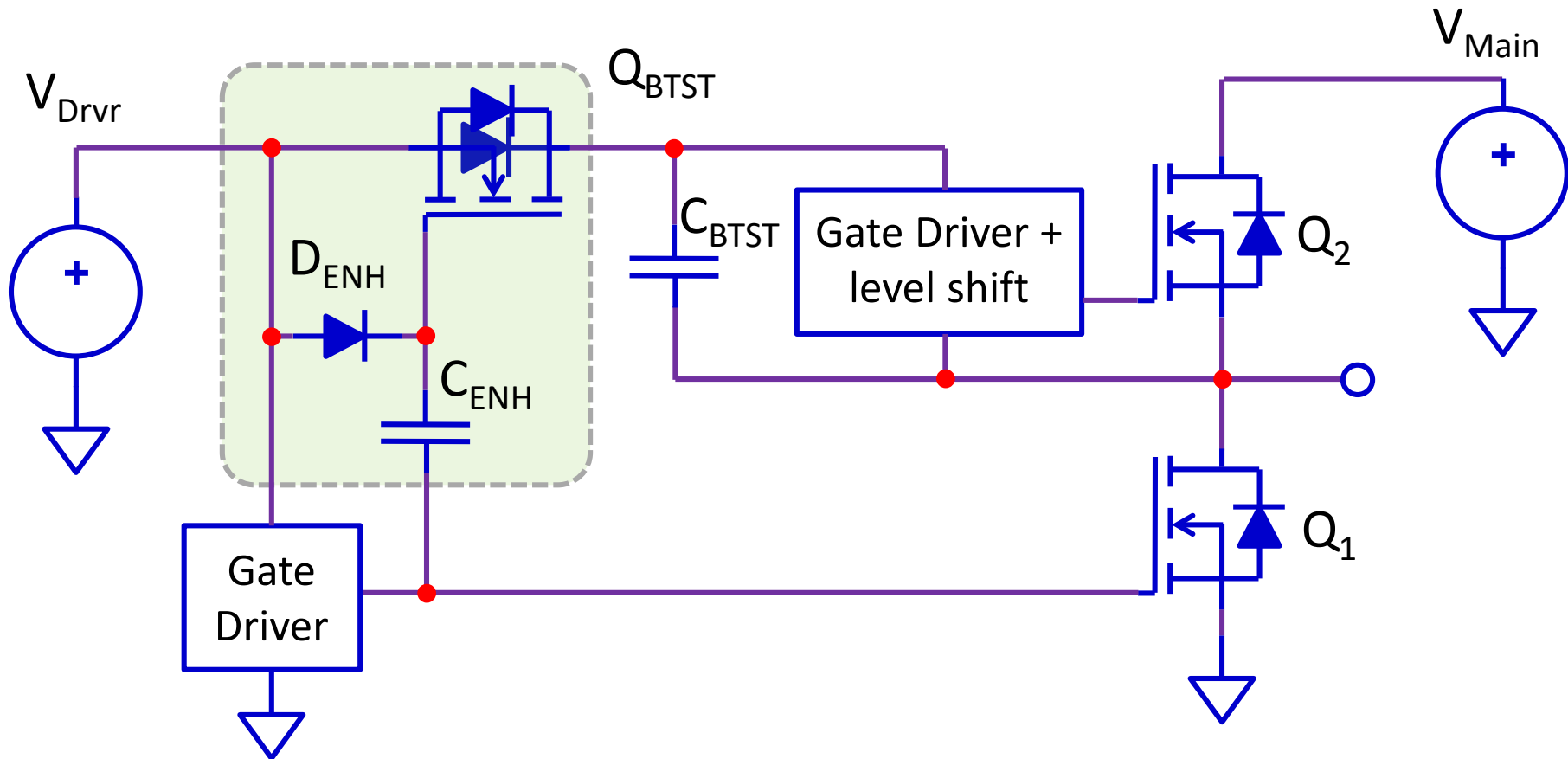
This  $Q_{RR}$  induces losses in the high side device:

- Proportional to frequency
- Present even with ZVS

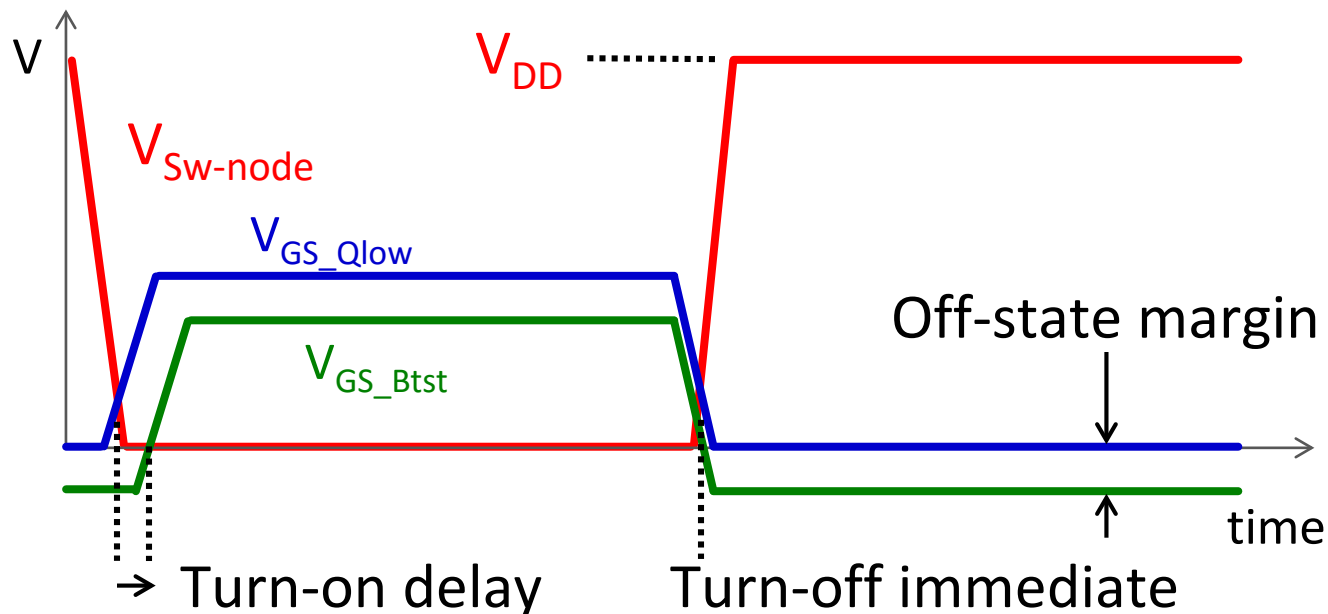


370 mW at 50 V, 6.78 MHz  
~ 40% of total FET losses

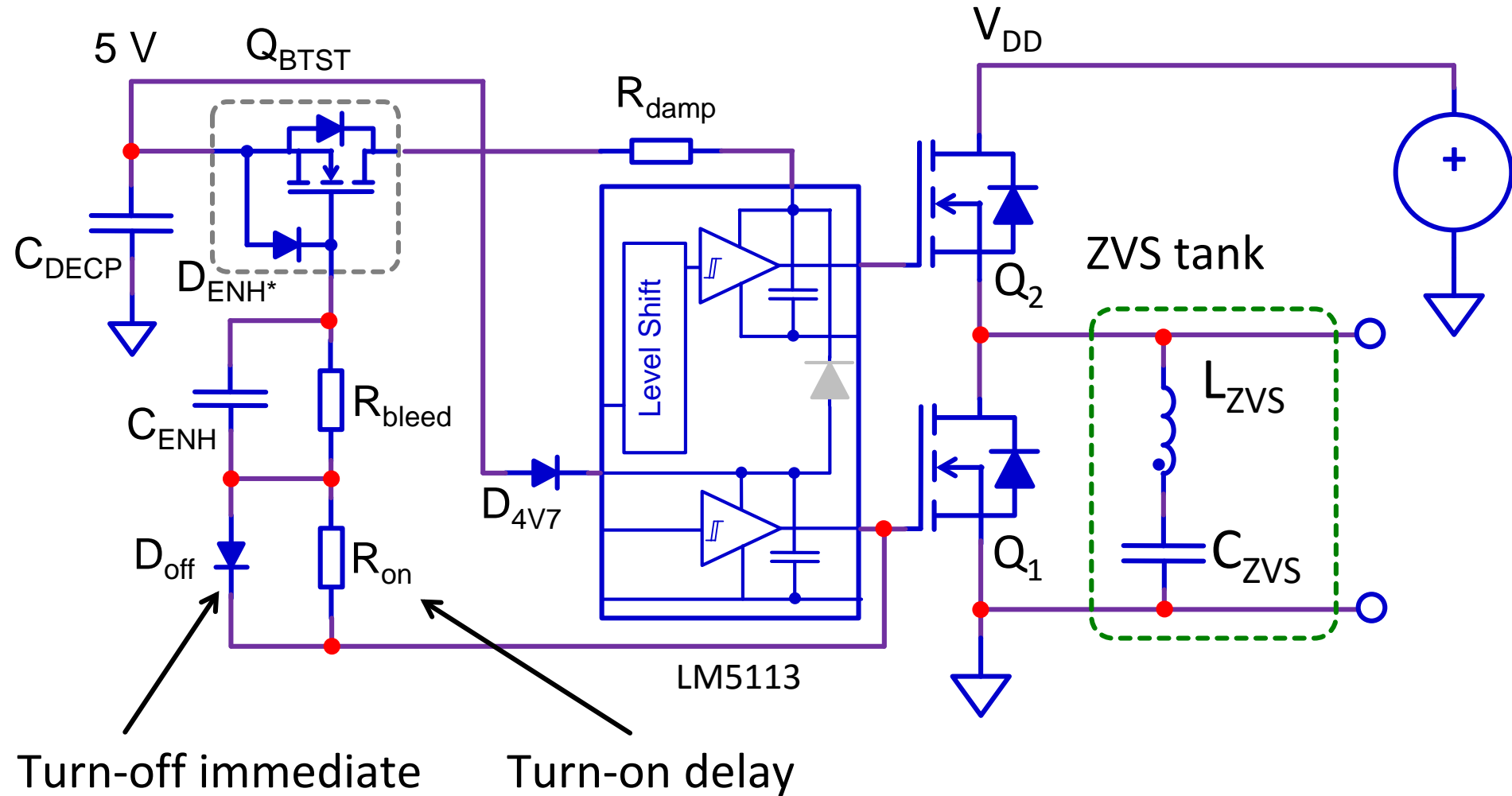
# Implementing a Synchronous FET Bootstrap Supply



- Timing:
  - Turn on - Delay
  - Turn off – Immediate
- Off state margin – if lower FET reverse conducts
- Drain inductance – to prevent over-voltage



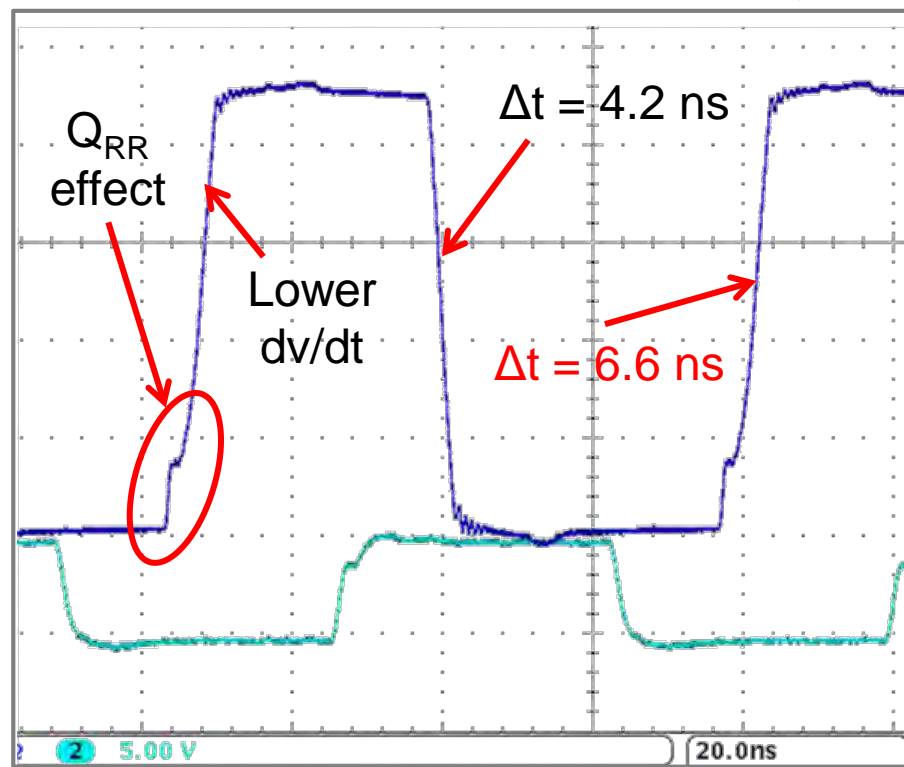
# Synchronous FET Bootstrap Supply Implementation



Reference: M. A. de Rooij, "Wireless Power Handbook," 2<sup>nd</sup> Edition, El Segundo, October 2015, ISBN 978-0-9966492-1-6.

# Waveform Improvements at 13.56 MHz Operation

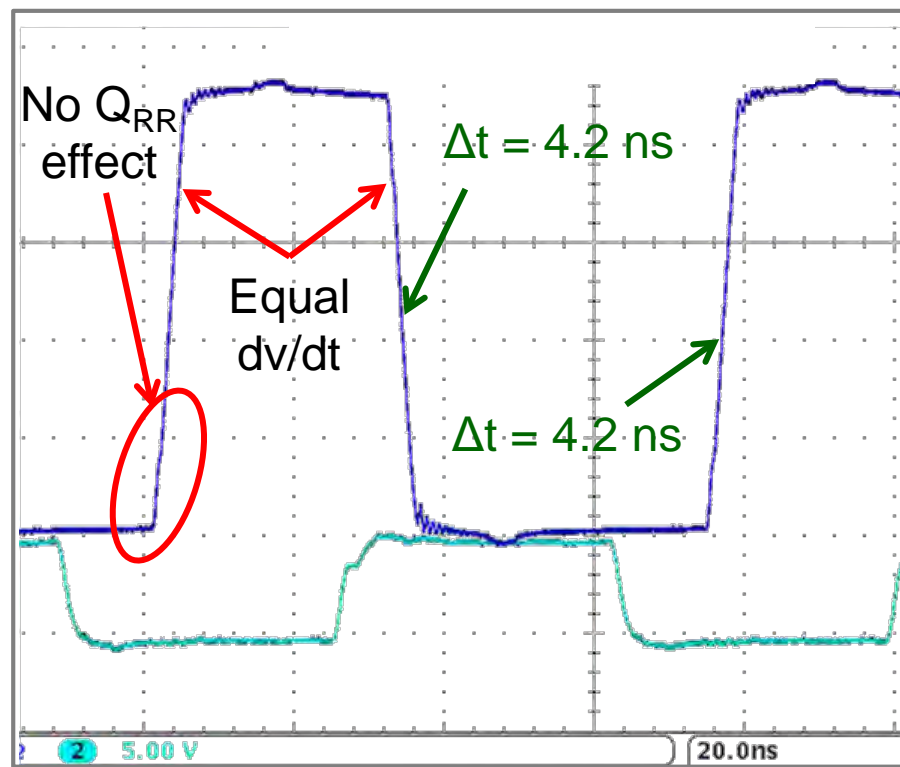
$V_{\text{supply}} = 45 \text{ V}$ , No load



5 V/Div.

20 ns/Div.

**Original Configuration**



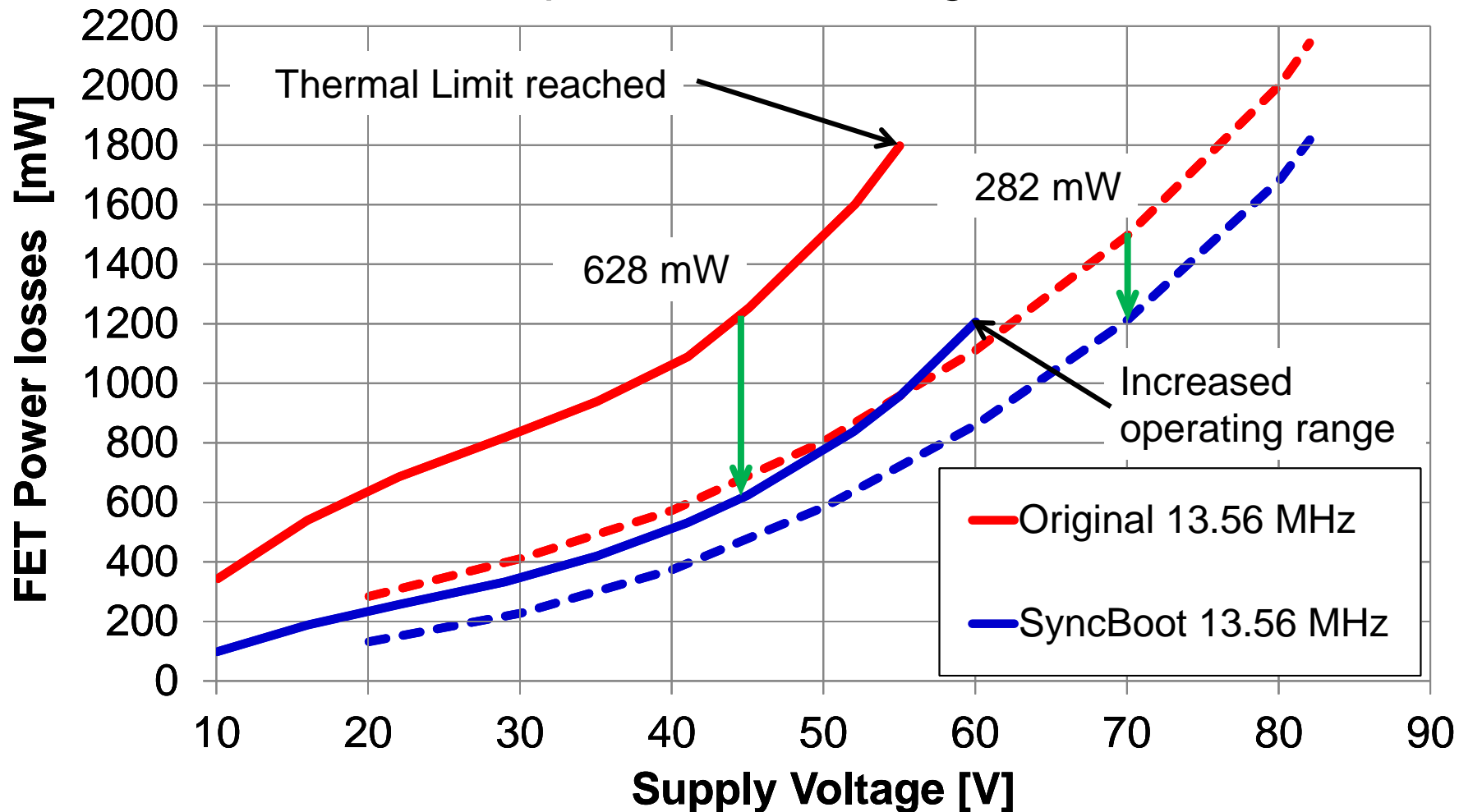
5 V/Div.

20 ns/Div.

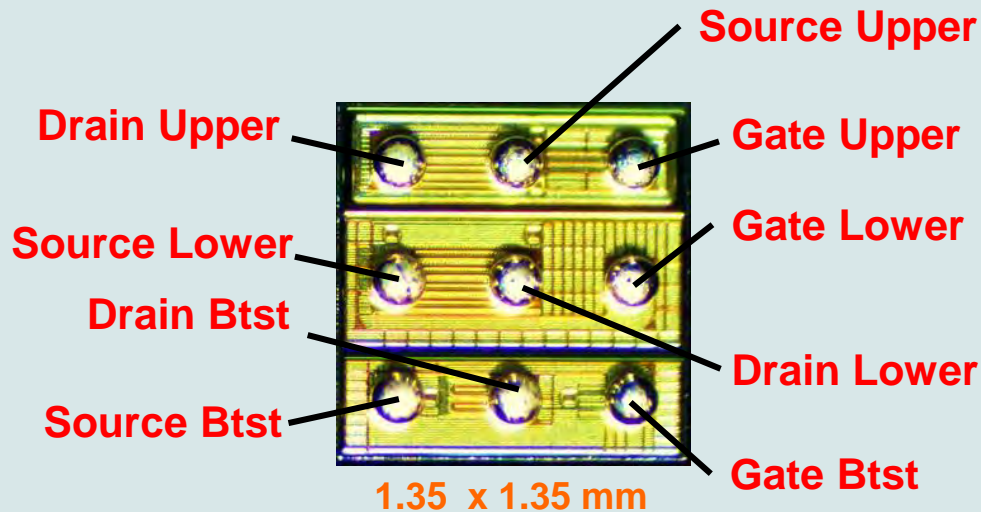
**Sync-Bootstrap Configuration**

- SW Node
- Gate Driver Input

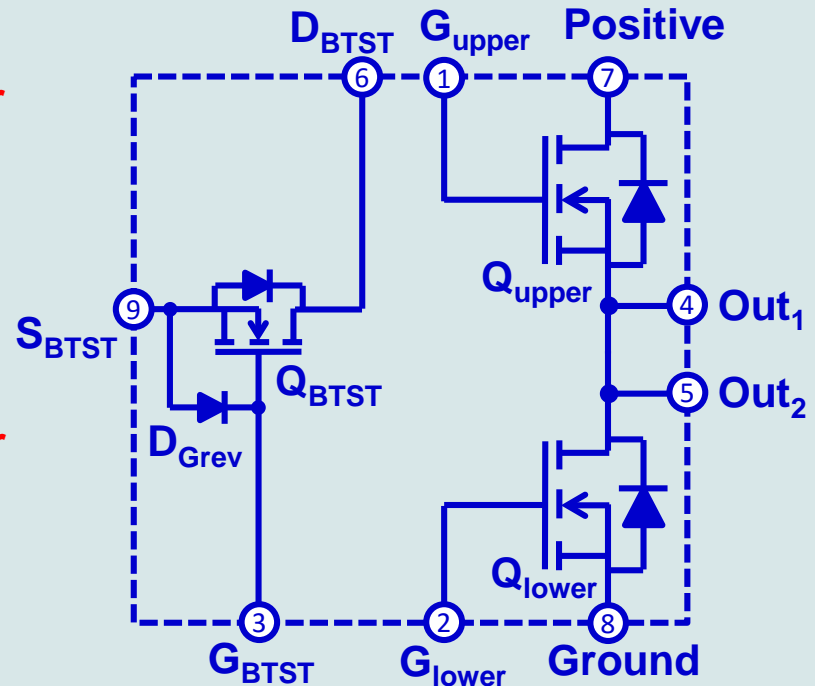
Total FET power, excludes gate driver







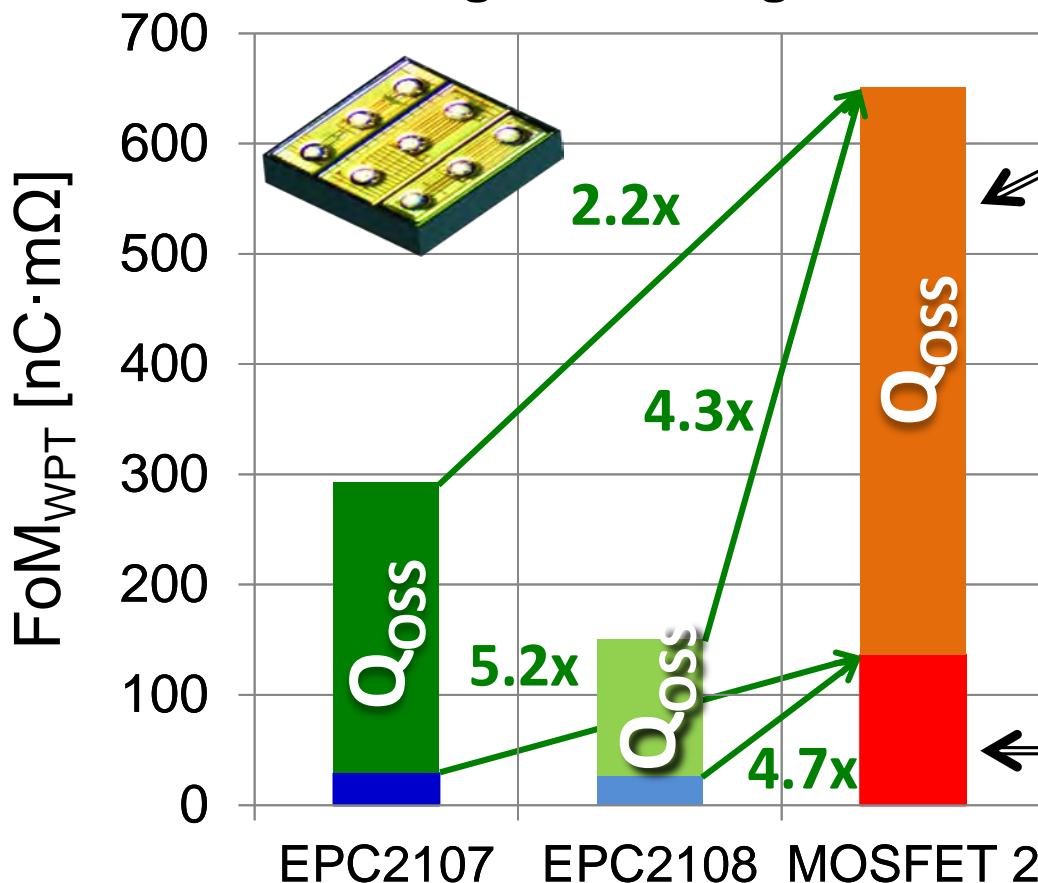
Solder Side View



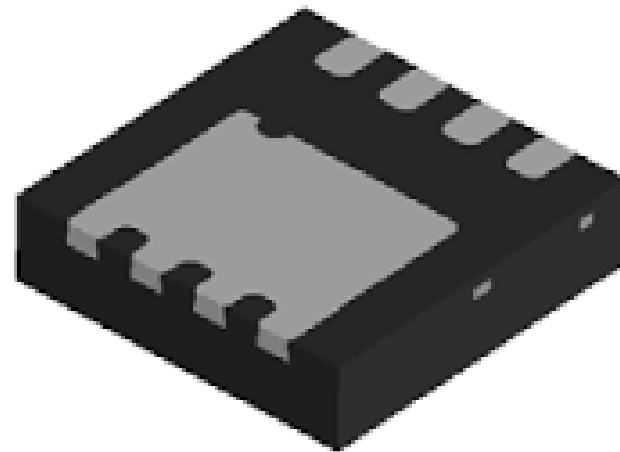
EPC Part Number	Package (mm)	$V_{DS}$ (V)	$V_{GS}$ (V)	$R_{DS(on)}$ @5V (m $\Omega$ )	$Q_G$ @5 V Typ. (pC)	$Q_{GS}$ Typ. (pC)	$Q_{GD}$ Typ. (pC)	$R_G$ Typ. ( $\Omega$ )	$V_{th}$ Typ. (V)	$Q_{RR}$ (nC)	$I_D$ (A)	$T_J$
												Max. ( $^{\circ}$ C)
<a href="#">EPC2107</a>	BGA1.35x1.35	100	6	240	160	65	40	0.6	1.4	0	1.7	150
	BGA1.35x1.35	100	6	240	160	65	40	0.6	1.4	0	1.7	150
	BGA1.35x1.35	100	6	2800	44	16	5	0.6	1.4	0	0.5	150
<a href="#">EPC2108</a>	BGA1.35x1.35	60	6	150	220	85	45	0.6	1.4	0	1.7	150
	BGA1.35x1.35	60	6	150	220	85	45	0.6	1.4	0	1.7	150
	BGA1.35x1.35	100	6	2800	44	16	5	0.6	1.4	0	0.5	150

$$FOM_{WPT} = R_{DS(on)} \cdot (Q_G - Q_{GD} + Q_{OSS})$$

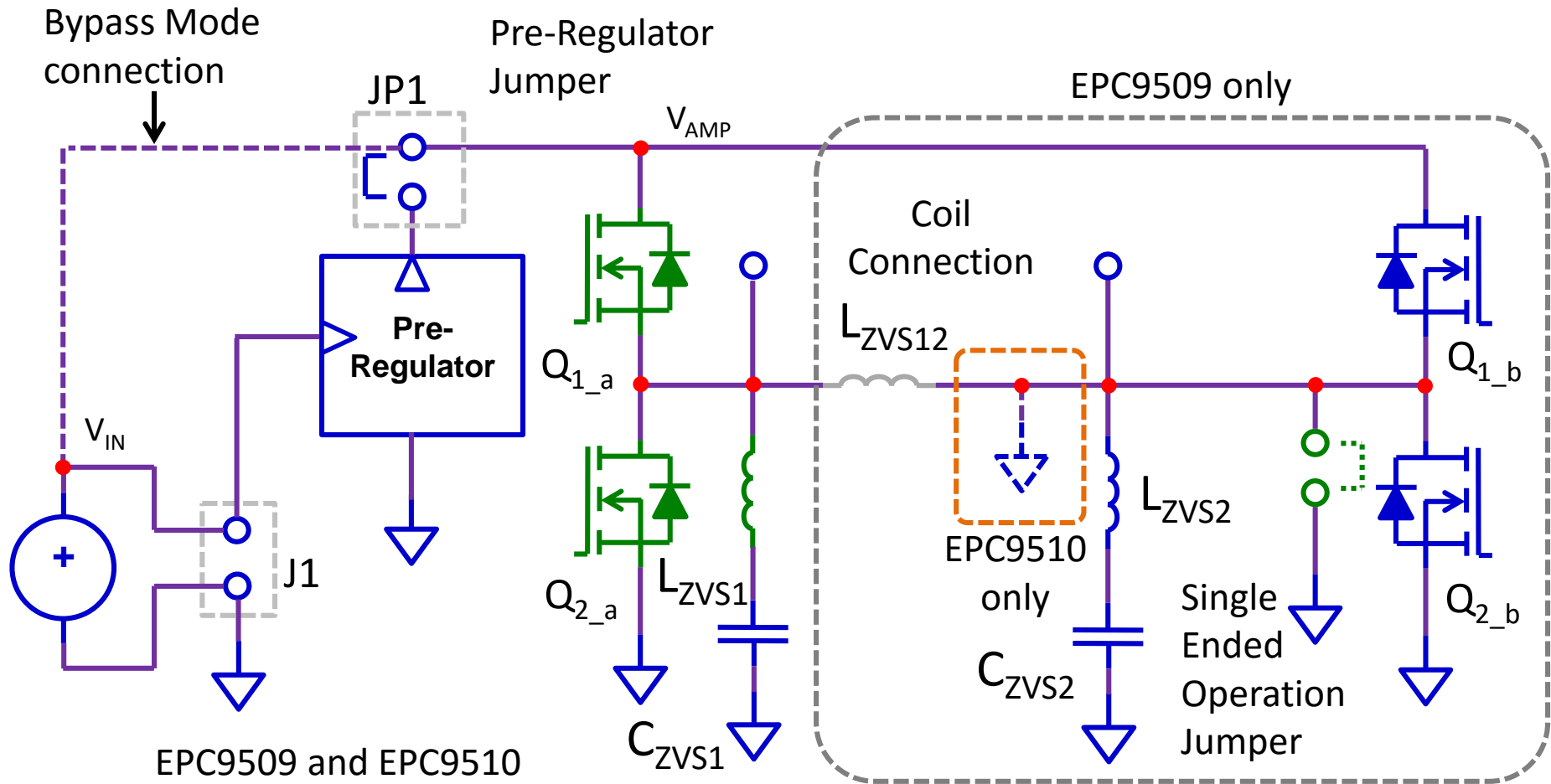
## Zero Voltage Switching Class D



$$FOM_{WPT} = R_{DS(on)} \cdot (Q_{OSS})$$



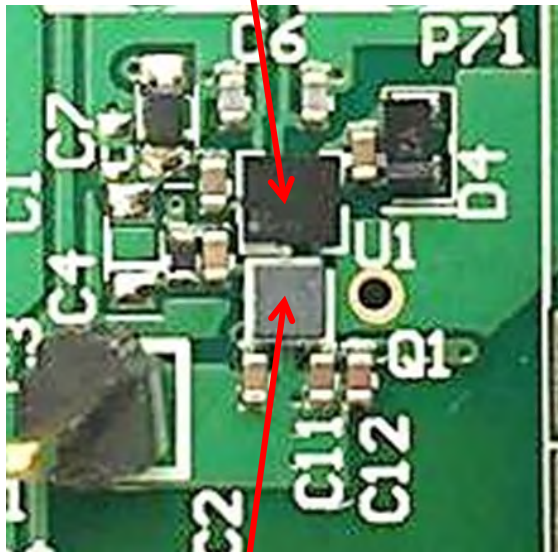
$$FOM_{WPT} = R_{DS(on)} \cdot (Q_G - Q_{GD})$$



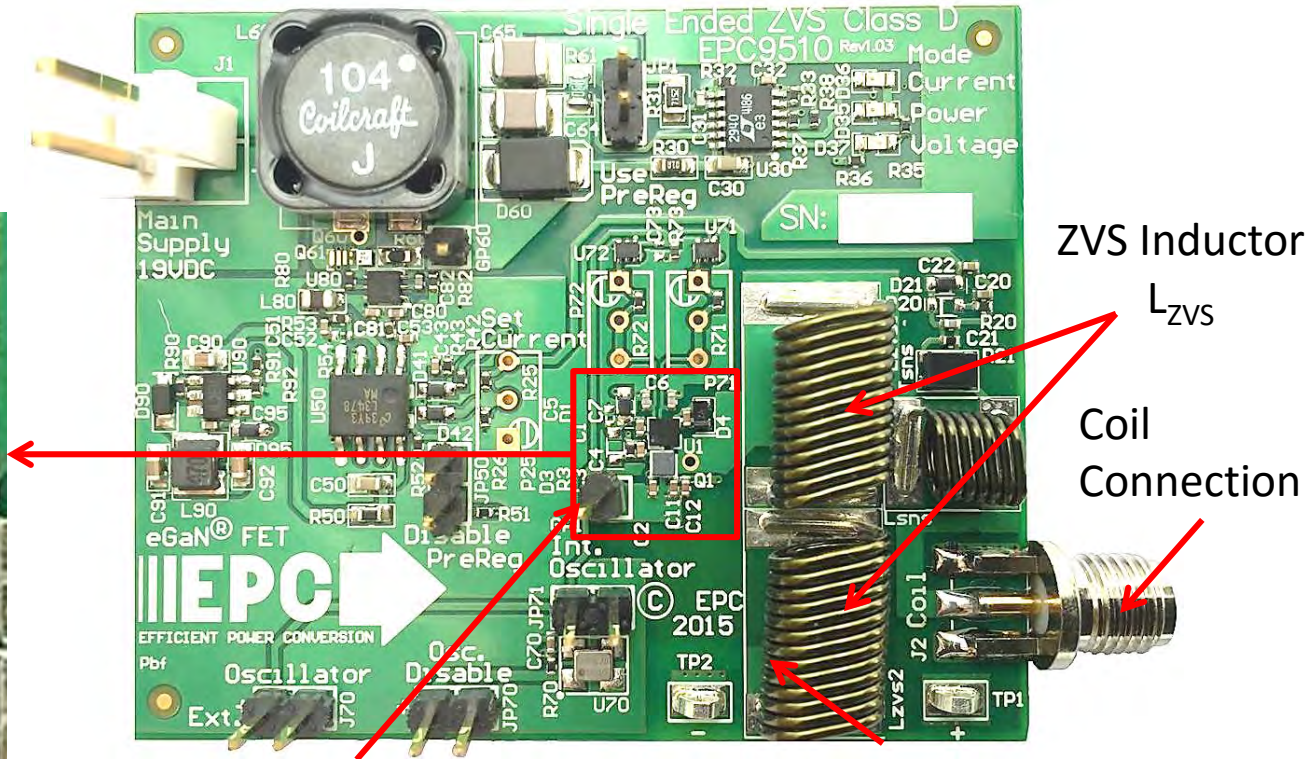
# Experimental Single-Ended ZVS Class D Amplifier

Configured for 6.78 MHz Operation

Gate Driver  
LM5113 (5 V)



eGaN IC



Oscilloscope  
Probe Post

EPC9510

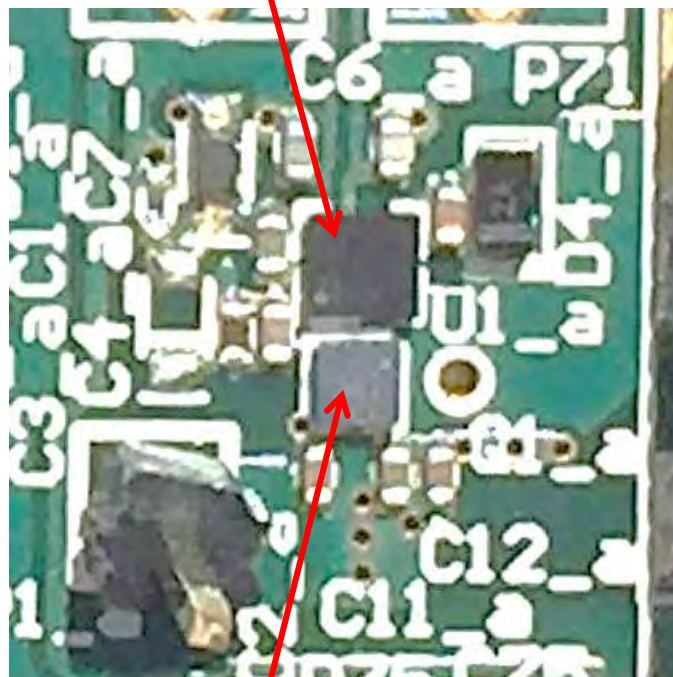
ZVS Capacitor  $C_{ZVS}$   
(Bottom Side)

EPC2107 – 100 V, 220 m $\Omega$ ,  $V_{GS} = 5 V$

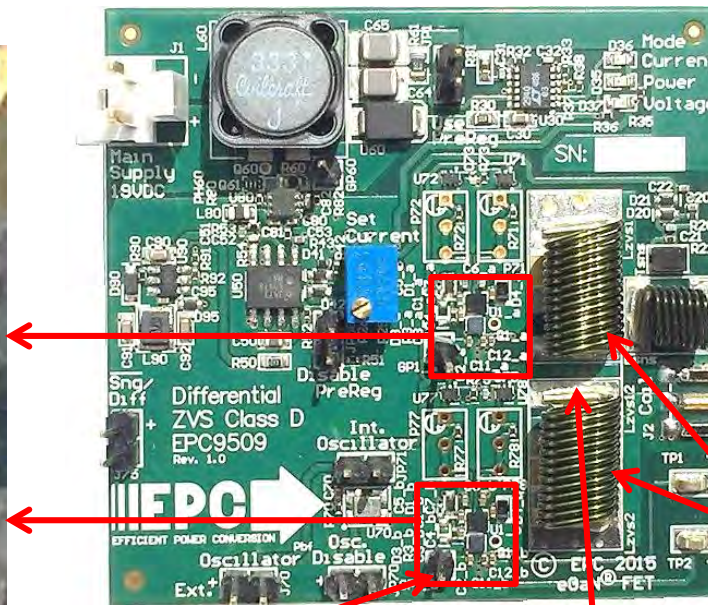
# Experimental Differential-Mode ZVS Class D Amplifier

Gate Driver  
LM5113 (5 V)

Configured for 6.78 MHz Operation



eGaN IC



Coil  
Connection

ZVS Inductor  $L_{ZVS}$

ZVS Capacitor  $C_{ZVS}$   
(Bottom Side)

Oscilloscope  
Probe Post

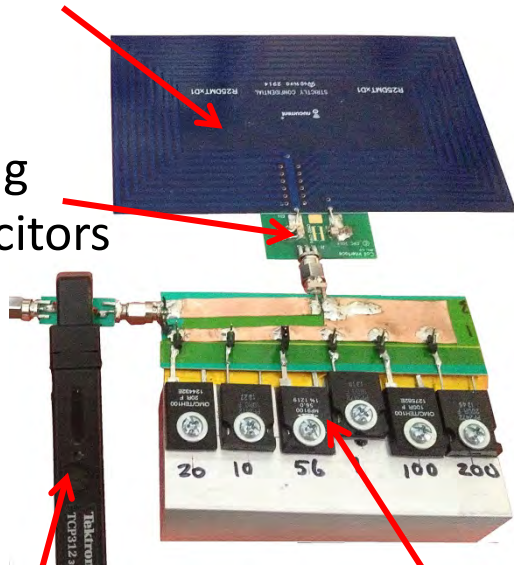
EPC9509

EPC2108 – 60 V, 150 m $\Omega$ ,  $V_{GS} = 5$  V

Measured at 6.78 MHz

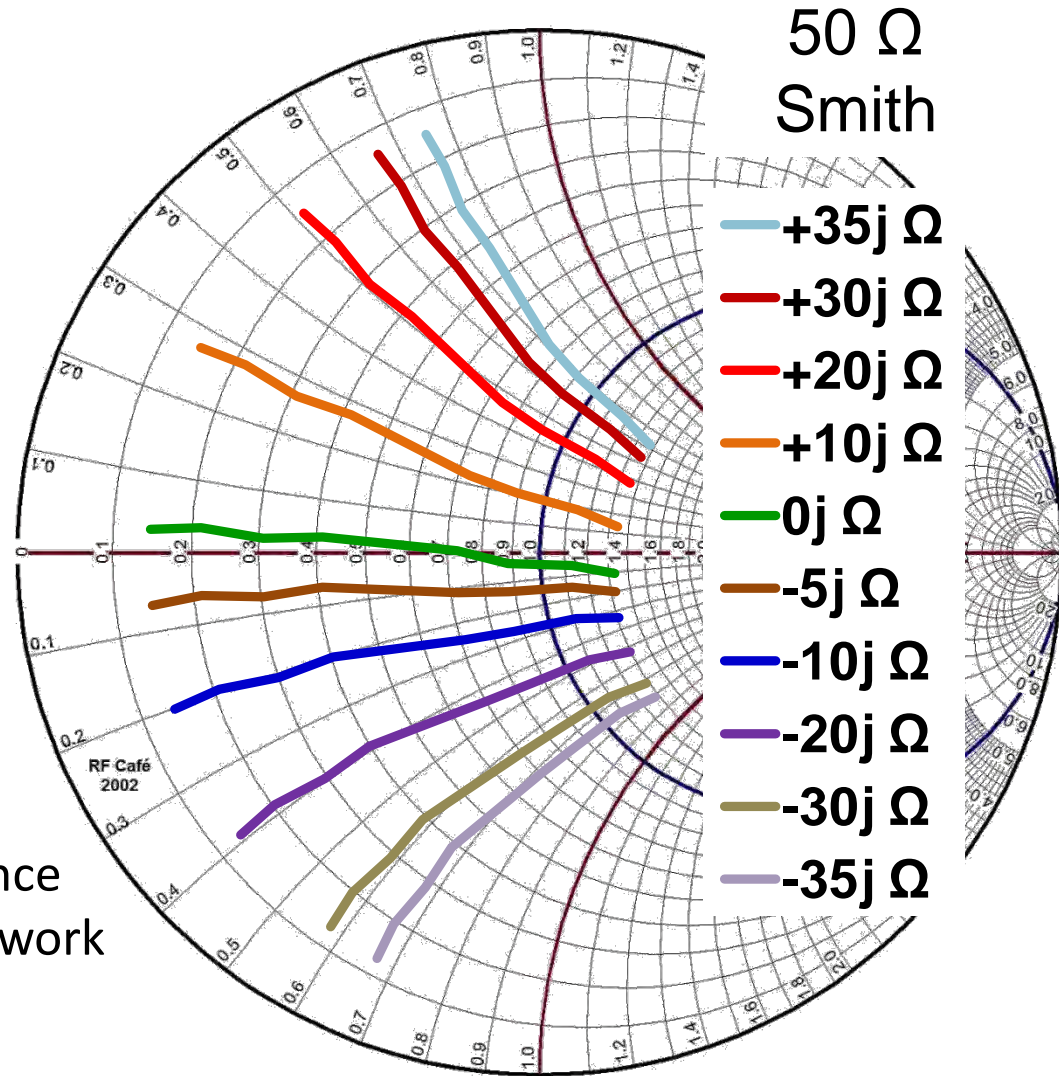
High Q coil used as an inductor ONLY

Tuning Capacitors



Current Probe

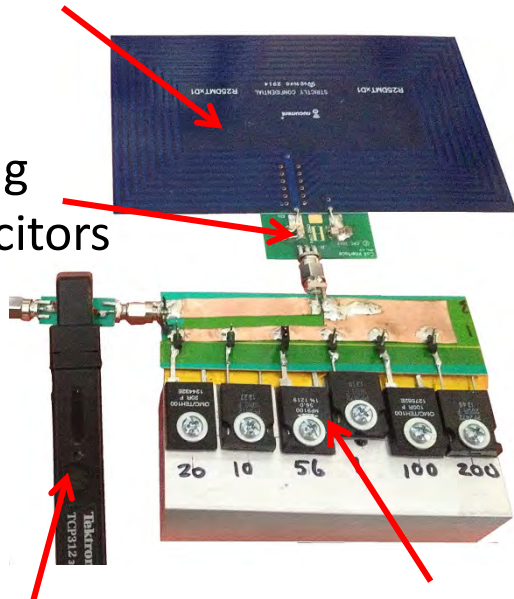
Low Inductance Resistance Network



Measured at 6.78 MHz

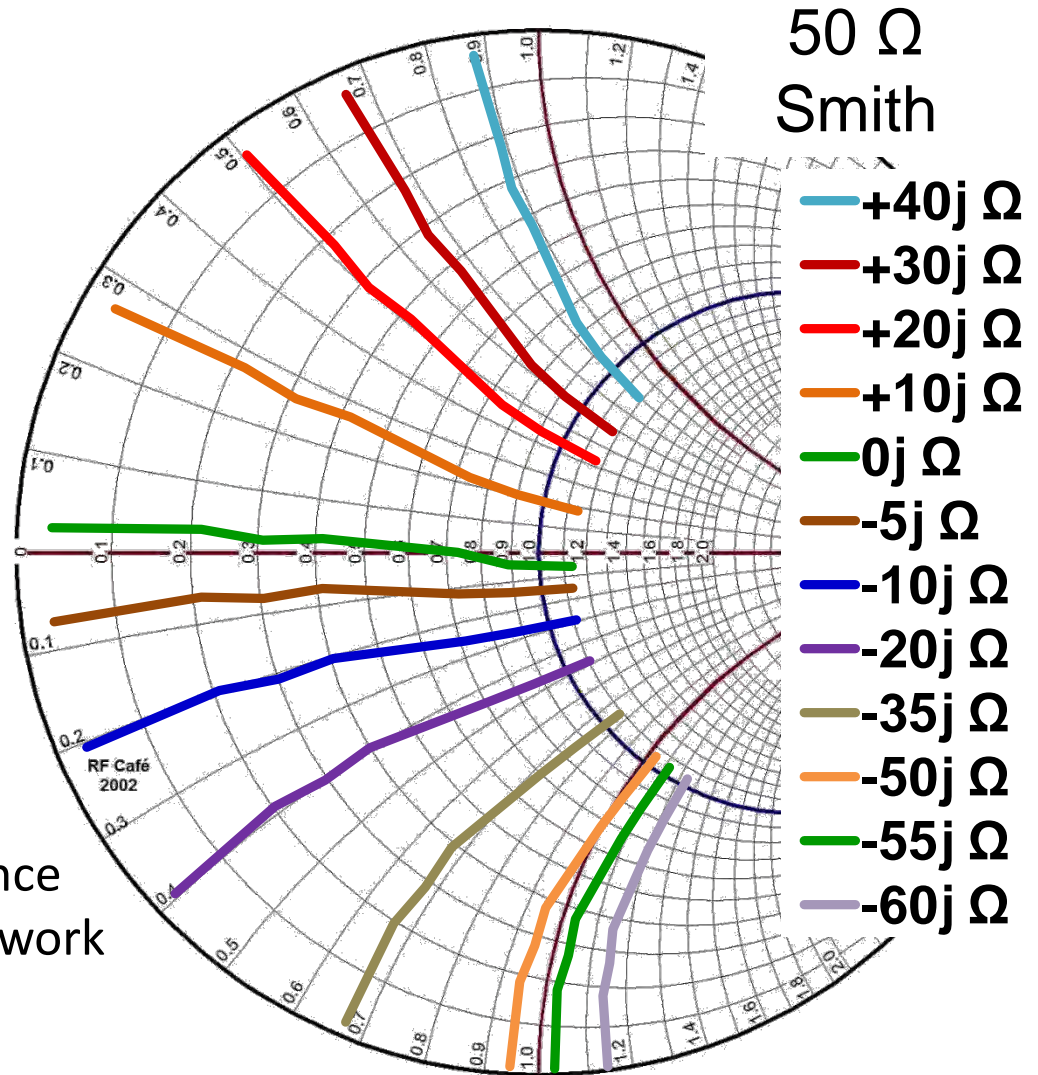
High Q coil used as an inductor ONLY

Tuning Capacitors



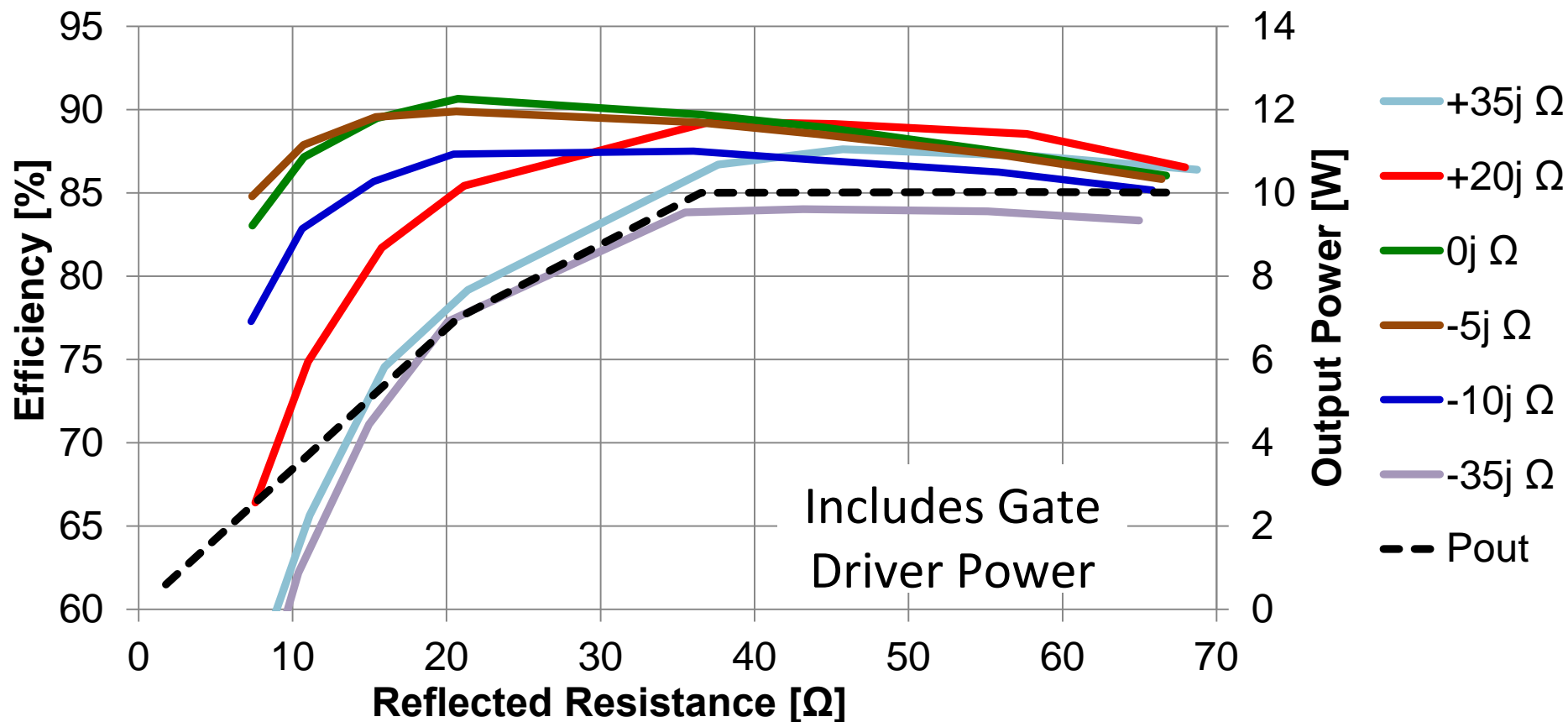
Current Probe

Low Inductance Resistance Network



Measured at 6.78 MHz

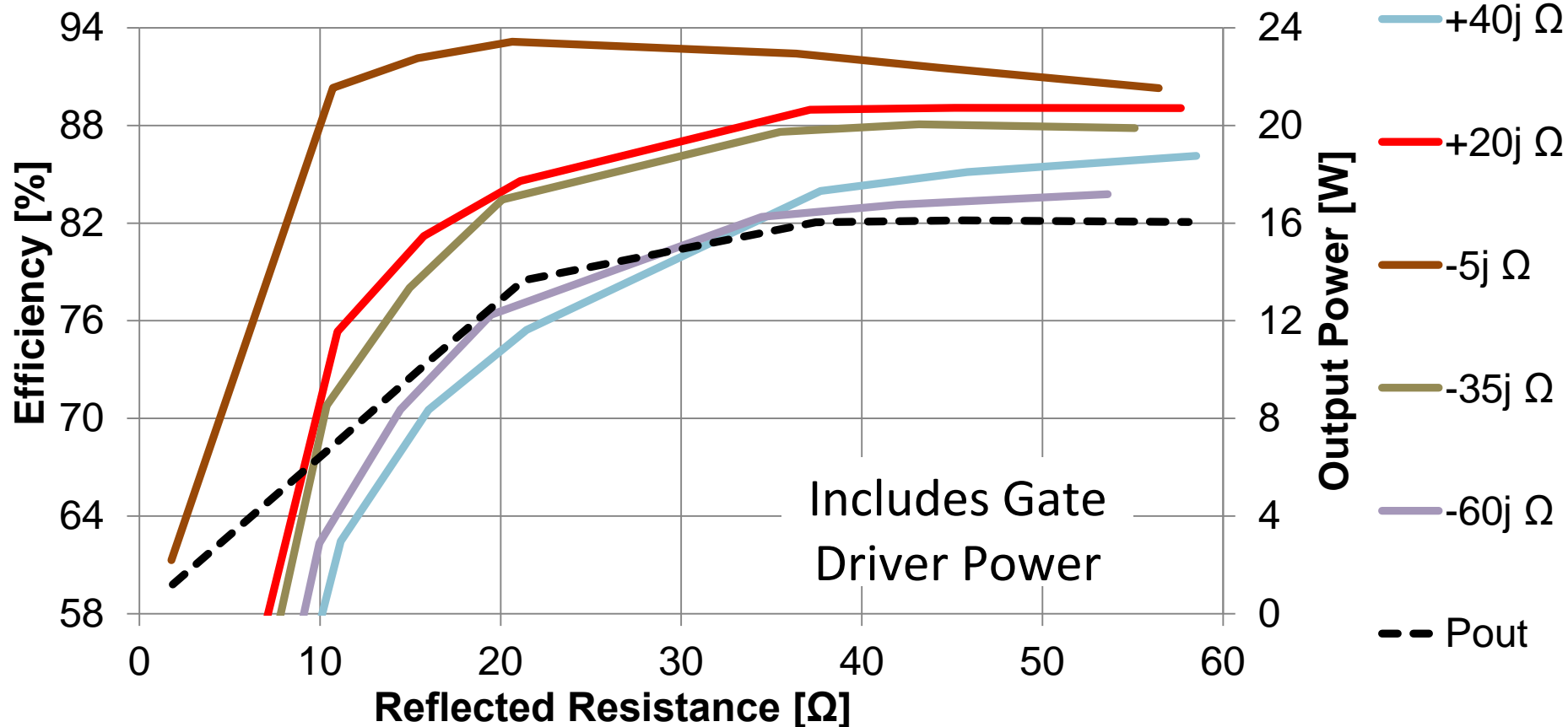
## EPC9510 Total Amplifier Efficiency





Measured at 6.78 MHz

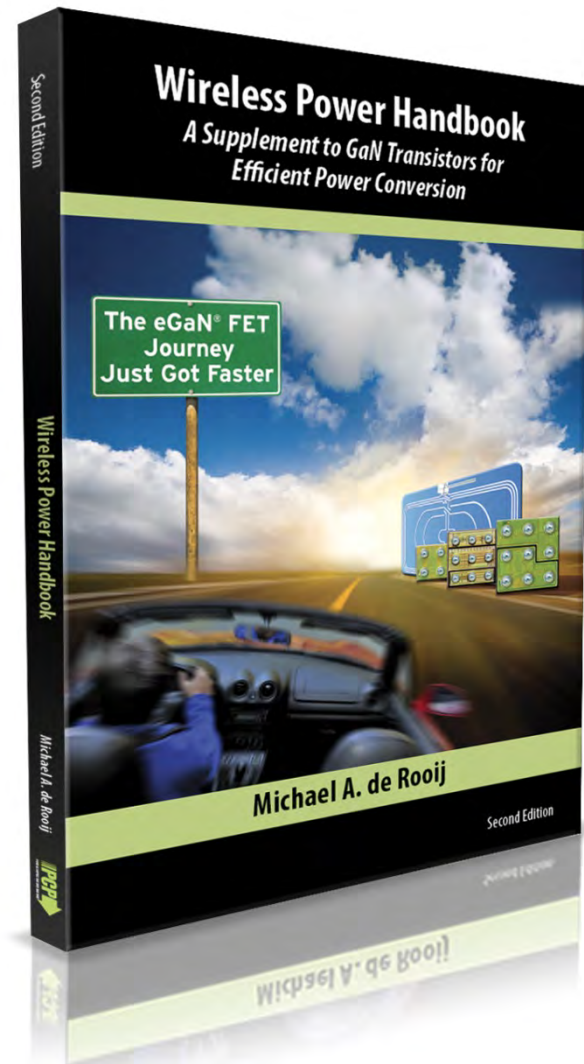
## EPC9509 Total Amplifier Efficiency



## Introduced the eGaN IC

- Lateral eGaN FET structure enables high voltage integration
- Three (3) FETs in one 1.35 mm x 1.35 mm chip-scale package
- Integration improves efficiency and power density
- Experimentally verified in a ZVS Class D amplifier
- eGaN ICs enable lower cost, higher performance wireless power

- Visit EPC's Booth #2244 to see several demonstrations in operation
- 2<sup>nd</sup> Edition Handbook on wireless power that covers this work and much more – available at Digikey (917-1136-ND) or Amazon



# EPC

EFFICIENT POWER CONVERSION

## *Where is GaN going...*

### Powering Innovation at the Speed of GaN

